## ABSTRACT

A data processing apparatus having data cache performs an 1 2 N-point radix-R Fast Fourier Transform. If the data set is smaller than the data cache, the data processing apparatus 3 performs the Fast Fourier Transform in  $\log_R\!N$  stages on all the 4 data set in one pass. If the data set is larger than the data 5 cache but smaller than R times the data cache, the data 6 processing apparatus performs a first stage radix-R butterfly 7 computation on all the input data producing R independent 8 intermediate data sets. The data processing apparatus then 9 10 successively performs second and all subsequent stage butterfly computations on each independent intermediate data 11 set in turn producing corresponding output data. During the 12 first stage radix-R butterfly computations, each of R 13 continuous sets are separated in memory by memory locations 14 equal to the size of a cache line. 15